SENIC Southeastern Nanotechnology Infrastructure Corridor

WEBINAR ANNOUNCEMENT

Etching at GT-IEN: A Review of Processes and Equipment

Mikkel Thomas, Ph.D.
Research Engineer
Senior Process Engineer
Georgia Tech Institute for Electronics and Nanotechnology (GT-IEN)

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Abstract: Etching is one of the fundamental building blocks of microelectronic fabrication. Removing material through chemical or physical means is an essential skill found in most microelectronics laboratories. The Institute for Electronics and Nanotechnology (IEN) at Georgia Tech offers a wide variety of tools and technologies to etch materials during a multitude of fabrication processes. Tools range from typical plasma enhanced etchers to vapor based etchers. With 15+ etch tools in the facility, IEN staff has the flexibility to configure each tool with a different selection of gases, which enables different etch capabilities and allow the IEN to segregate processes within the facility. In this seminar, a brief introduction to the tools and technologies available in the IEN cleanrooms will be presented. Common etching issues and concerns will also be discussed.

Bio: Dr. Mikkel Thomas has worked for the Institute for Electronics and Nanotechnology since 2008. He earned a Bachelor of Science in Electrical Engineering in 1997, a Master's of Science in Electrical Engineering in 1999 and a Ph.D in Electrical Engineering with a specialization in Optoelectronics in 2008, all from the Georgia institute of Technology. Prior to his employment at Georgia Tech, Dr. Thomas worked at OptiComp Corporation located in Zephyr Cove, Nevada. His research at the company revolved around the development of a VCSEL based, integrated optical communication system for use in satellites and other aerospace applications. Since arriving at Georgia Tech, in the IEN, Dr. Thomas provides cleanroom processing support to the academic faculty and their graduate students. He also provides processing support and fabrication services for entities not directly affiliated with the institute. He is the current lab instructor for ChBE 4050.

Who should attend: Faculty, scientists, engineers, researchers, and technical staff from university, company, or government labs who use, or are interested in learning about, micro- and nano-scale fabrication and characterization as part of their research efforts.

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